

Curriculum Vitae

Dr. Mohanasundaram Ranganathan BE., ME., Ph.D.,

Associate Professor
School of Computer Science and Engineering,
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ACADEMIC / PROFESSIONAL BACKGROUNDS (11 Years 6 Months)

- **VIT UNIVERSITY**, Vellore, Tamilnadu, India from 02.01.2013 to till date
 - Associate Professor from 01/07/2017 to till date
 - Assistant professor senior from 04/12/2015 to 31/05/2017
 - Assistant professor from 02/01/2013 to 03/12/2015
- **K.S.R. College of Engineering**, Tiruchengode as Assistant Professor from 01.12.2010 to 31.12.2012
- **Velalar College of Engineering and Tech**, Erode as Lecturer from 02.06.2008 to 30.11.2010
- **Nandha Engineering College**, Perundurai as Lecturer from 08.02.2008 to 31.05.2008

EDUCATIONAL BACKGROUND

- **Ph.D.**, (Electrical Engineering - Jan 2012 to Dec 2015 – External/Part Time) Anna University - Chennai, TN, India.
- **M.E** (Embedded System Technologies - June 2006 to April 2008 – Full Time), Velalar College of Engineering and Technology, Erode, TN, India
- **B.E.** (Electrical and Electronics Engineering - June 2003 to April 2006 – Full Time), Velalar College of Engineering and Technology, Erode, TN, India
- **Diploma** (Electrical and Electronics Engineering - June 2001 to April 2003 – Full Time), MPNMJ Polytechnic College, Chennimalai, Erode, TN, India

PROFESSIONAL MEMBERSHIP

- IEEE Member, (ID: 95448037)
- Life Member, ISTE (ID: LM65275)
- Life Member, CSI (ID: I1501938)

AWARDS / APPRECIATIONS:

- Research Award 2017 for Publications, VIT University, India on 19th Oct 2018.
- “Best Academician of the Year” award from Research Excellence and Academic Award - 2018 organized by Combined Society for Educational Research and Development (CSERD), Dehradun on March 10, 2018.
- Research Award 2016 for Publications, VIT University, India during April 2017.
- Certificate of appreciation for the contribution towards successful completion of IQA (Internal Quality Audit) for the AY 2016- 17.
- Certificate of appreciation for the contribution as Faculty Coordinator in GRAVITAS’17, VIT, Vellore.
- Research Award 2015 for Publications, VIT University, India on April 2016.
- Research Award 2014 for Publications, VIT University, India on April 2015.
- Certificate of Appreciation for best organizer, International conference on computing, cybernetics and intelligent Information Systems (CCHIS’ 2013), VIT University, India.

PEER-REVIEWER:

- Wireless Personnel Communications (Springer), International Journal of Circuits and Systems, International Journal of Engineering Technology, Journal of Testing and Evaluation, Bonfring publications, Inderscience-International Journal of Advanced Intelligence Paradigms (IJAIIP), Bio-Medical Research, INASS.

INVITED LECTURE GIVEN:

- 27/07/2018, Asian College of Engineering and Technology, Coimbatore, Tamil Nadu, India - Chief guest cum Keynote speaker for EEE department association inaugural function.
- 03/03/2018, Keynote speaker in “International Conference on Recent Technologies in Electrical Engineering (ICRTEE – 2018), ARM College of Engineering and Technology, Chengalpattu.
- 16/02/2018 and 17/02/2018, Keynote Speaker in “ICMR Sponsored Two Days National Seminar on Big Data Analytics and IOT for Healthcare Applications” organized by Department of CSE, Malla Reddy College of Engineering and Technology, Secunderabad.
- 15/12/2017, Velalar College of Engineering and Technology, Erode, TN, India – Given a Session in seven days FDP on “Embedded system applications in routine life”.
- 06/03/2017, Knowledge Institute of Technology, Salem, TN, India – Guest lecture on “Challenging questions on Digital Logic”.
- 12th April, 2017, Navodaya Institute of Technology, KA, India – Chief guest and Keynote speaker for an FDP on "Embedded System And Its Applications".
- 27 January 2015, MPNMJ Engineering College, TN, India – “Digital Principles”.

- 05 March 2016, KSR College of Engineering, TN, India – Chief Guest and Keynote speaker for a national conference on cutting edge technologies in information and communication.
- 28 March 2016, KSR College of Engineering, TN, India- “Soft tools for Digital Logic - Quatrus-II”.

RESEARCH INTEREST AND ACCOMPLISHMENTS:

- Wireless sensor networks – Energy optimization
- Swarm Intelligence – Positioning of Storage node
- Big data – data analysis
- Embedded systems – Intelligent Buildings
- Signals processing – Voice recognition
- Very large scale integration – VLSI design
- Energy conservation – power factor management, stabilizer

FUNDED PROJECTS:

- Project member/Consultant PI: Valorization of recycling waste into biofuels experimental and modeling aspects, FOA-2018-8183, Ericyes University, Turkey (24 months), Amount: 106040,66 Turkish Lira, (Start date: Dec 1, 2018)

PROJECTS:

Master Degree project: **Embedded Based Home PC Access and Control using VNC through GSM** - This project is to control and access our home personal computer by using VNC (Virtual Network Computing) from anywhere in the world.

Under Graduate Project: **Implementation of Multi-rate FIR Filter in FPGA** - The objective of this project is to develop a FPGA based system, which implements the Multirate FIR Filter. This allows a signal to pass from input to output according to the frequency of the signal. Frequencies above the pass band are attenuated with a high degree of attenuation.

Diploma Project: **5KV Automatic Voltage Stabilizer**

PROJECTS GUIDED:

- Guided More than 38 numbers of Engineering Projects, 8 M.Tech Project.
- At present guiding 6 Ph.D (by Research) candidates and 1 M.Tech (by research) candidate.
- Currently guiding the following B.Tech design projects
 - *IOT Based Live environmental condition monitoring system using embedded sensors*

- *Robotic Wheelchair Control by Head Orientation and Eye ball Movement control with Health Monitoring System*
- *Smart helmet for carbon dioxide and alcohol detection and vehicle tracking system*
- *System for Precision farming enabling auto irrigation and analysis using Internet of Things*
- *IOT Based Human Less Tollgate Collection With Low Balance Notification And Theft Alert Using NFC*
- *Finger Recognition Based Device Control For Paralyzed Patients Using IP Camera*
- *Music Generation Using Deep Learning Techniques*
- *IOT robotics control based industrial parameter monitoring*

PUBLICATIONS:

Google scholar profile: <https://goo.gl/KyDhbd>

** Three more papers has been communicated*

PUBLICATIONS – SCI/SCOPUS/WOS INDEXED:

1. Deepak Raj Munirathinam, **Mohanasundaram Ranganadhan**, “A new improved filter-based feature selection model for high-dimensional data”, The Journal of Supercomputing, ISSN-1573-0484, pp- 1-18, August 2019.
2. **R. Mohanasundaram**, R. Arun, K.R. Jothi, Ankit Malhotra, M.P. Gopinath and R. Lokeshkumar, “Web application for Accident Emergency in Nearby Hospitals and Donor Locator”, Biosci. Biotech. Res. Comm. Special Issue Vol 12 No (2), ISSN: 2321-4007, March 2019.
3. R. Prathap and **R. Mohanasundaram**, “Electronic Health Records (EHR) and Cloud Protection: The Present Problems”, Journal of Computational and Theoretical Nanoscience, ISSN: 1546-1963, Vol. 15, pp.2227–2232, June 2018.
4. **R. Mohanasundaram**, Anirudh Batra, Kashish Arora, and Adwaith Sreekumar, “Internet of Things Based Cooperative Hazard Awareness and Avoidance System for Safer Roads and Prevention of Traffic Jams Due to Accidents, Journal of Computational and Theoretical Nanoscience, Vol. 15, 2466–2470, June 2018.
5. S. Divya, Adwaith Sreekumar, **R. Mohanasundaram**, M. Srinivasan, “Web Application Trap Password for Safe Online Transactions”, Journal of Computational and Theoretical Nanoscience, Vol. 15, 2461–2465, June 2018.
6. J. Bagiya, Adwaith Sreekumar, P. V. Vidya Varshini, and **R. Mohanasundaram**, “Automated Task Assignment on Multi-Skill Oriented Spatial Crowd Sourcing”, Journal of Computational and Theoretical Nanoscience, Vol. 15, 2364–2368, June 2018.
7. Shivin Sinha, Arpit Singhal, **R. Mohanasundaram**, H. Abdulgaffar and Navin Kumar, “Communication System for Vocationally and Visually Disabled People Using Embedded System”, Journal of Computational and Theoretical Nanoscience, Vol. 15, 2082–2088, June 2018.
8. **Mohanasundaram R**, N. Suresh Kumar, V.Ashok, Poorani.S, “Congestion Control Using Congestion Free Routers”, International Journal of Pure and Applied Mathematics, ISSN: 1314-3396, Volume 117, No. 15, pp 287-298, December 2017.

9. **R.Mohansundaram**, M. Thenmozhi, AdwaithSreekumar, K.S. ArunAdithiyan, “Reduction of Data Redundancy Using Data Aggregation in Wireless Sensor Networks”, International Journal of Pure and Applied Mathematics, Volume 117, No. 15, pp 299-312, December 2017.
10. M.P.Gopinath, G.S. Tamilzharasi, S.L.Aarthy and **R.Mohanasundram**, “ An Analysis and Performance Evaluation of NOSQL Databases for Efficient Data Management in E-Health Clouds”, International Journal of Pure and Applied Mathematics, Volume 117, No. 21, pp 177-197, December 2017.
11. Sujatha R, Dr Xhia-Zhi Gao, M P Gopinath, S Prabu, S Murali, R Mohanasundaram, N Suresh Kumar, “**A Computer Aided System To Detect Breast Masses Using Digital Mammograms**”, International Journal of Civil Engineering and Technology (IJCIET), Volume 8, Issue 12, December 2017, pp. 309–315.
12. **Mohanasundram.R**, Shraddha Chaturvedi, Brijkishore Dutt, Rishabh Sudan, , “Cloud Based Weather Monitoring System”, Jour of Adv. Research in Dynamical and Control Systems, Vol: 9 SI: 6, pp:298-308, 2017.
13. V.Ashok, P.P.Janarthanan, **R.Mohanasundaram**, “Certain Investigations On EC-A Algorithm For Energy Minimization In Multiprocessor Systems”, Jour of Adv. Research in Dynamical and Control Systems, Vol: 9 SI: 6, pp: 281-290, 2017.
14. M.P.Gopinath, S.L.Aarthy, S.Prabu, **R.Mohanasundram**, “Visualising The Extracted Features Of Face Images Using LBP and LTP”, Jour of Adv. Research in Dynamical and Control Systems, Vol: 9 SI:6, pp:254-262, 2017.
15. Dr.A.Jayanthiladevi, **Dr.Mohanasundaram**, Dr. K. Sakthivel, “E-Agricultural Market”, International Journal of Control Theory and Applications, 9(28), pp. 1-8, 2016.
16. **R.Mohanasundaram**, Atana Iyer D, Suganthi A, “A Gas leakage detection system based on Embedded systems”, International Journal of Pharma and Bio Sciences ISSN 6299-0975 special issue in computational biological data science, June 2016, pp. 68-72.
17. P.Anitha, **R.Mohanasundaram**, P.S.Periasamy, “Optimization Methods for Data Storage Position in Wireless Sensor Networks: A Survey”, Asian Journal of Information Technology Medwell Journals, (ISSN: 1993-5994), Vol 15, Issue: 13, pp. 2238-2243, 2016.
18. **R.Mohanasundaram**, P.S.Periasamy, “A Meta heuristic Algorithm for Optimal Data Storage Position in Wireless Sensor Networks”, Pakistan Journal of Biotechnology (ISSN : 1812-1837), special issue on Innovations in information Embedded and communication Systems) Pp. 463-468 (2016).
19. **Mohanasundaram Ranganathan**, and Pappampalayam Sanmugam Periasamy. "Hybrid Swarm Intelligence Optimization Approach for Optimal Data Storage Position Identification in Wireless Sensor Networks." The Scientific World Journal 2015 (2015).
20. **Mohanasundaram R**, P.S.Periasamy, “Clustering Based Optimal Data Storage Strategy Using Hybrid Swarm Intelligence In WSN”, Wireless Personal Communications, SPRINGER 2015
21. **R.Mohanasundaram**, P. S. Periasamy “Swarm Based Optimal Data Storage Position Using Enhanced Bat Algorithm In Wireless Sensor Networks” International Journal of Applied Engineering Research ISSN 0973-4562 Volume 10, Number 2 (2015) pp. 4311-4328.

1. D. M. Deepak Raj , **Dr. Mohanasundaram Ranganathan**, “A Comprehensive Survey On Erasable Itemset Mining”, International Journal of Computer Science and Information Security (IJCSIS), Vol. 15, No. 7, pp 184-201, July 2017.
2. **R. Mohanasundaram**, Sadulla Shaik (University of Malaya, 50603 Kuala Lumpur, Malaysia), S.Murali, M.P.Gopinath, “A Trusted Database On Enquipment With Protection And Information Privacy”, International Journal of Communication and Computer Technologies, Volume 04– No. 14 Issue: 04 Dec 2016, ISSN NUMBER : 2278-9723
3. **R.Mohanasundaram**, V.R.Raghuveer ,“Improving the Effectiveness of Learning Content Retrieval through Content Classification and Profiling”, International Journal of Computer Science and Mobile Computing, Volume 3, Issue 8, August 2014
4. **R. Mohanasundaram**, K. Aarthi Priyamvadha , “An Efficient Deduction Of Data Loss Rate In Wireless Sensor Network”International Journal of Modern Engineering Research (IJMER), Vol. 3, Issue. 3, pp-1649-1653 May - June 2013.
5. R.Arun, **R.Mohanasundaram**, “EGMP: Multicasting Routing Protocol with QoS Improvements”, Bonfring International Journal of Research in Communication Engineering Volume: 2 | Issue: Special Issue 1, Pages: 98-101. February 2012.
6. **R. Mohanasundaram**, E.K. Kavithaashri. “ Small Delay Defect Detection Using Signature Register”, Bonfring International Journal of Power Systems and Integrated Circuits, Vol. 2, Special Issue 1, Part 2, Pages: 60-67, February 2012.
7. B.Sudharsan, **R.Mohanasundaram**, “Observing and Control of Agricultural Parameterns by using Wired/Wireless Networking Infrastructure”, International Journal of Advanced Research n Technology, Vol.2, Issue 2, 2012, Pg No: 170-176.

INTERNATIONAL CONFERENCES:

1. R Prathap, **R. Mohanasundaram**, P Ashokkumar, “Design of EHR in Cloud with Security”, Smart Intelligent Computing and Applications, Part of the Smart Innovation, Systems and Technologies book series (SIST, volume 105), pp 419-425, (2018)
1. A.Mummorthi, R.Mohanasundaram, Shubam Saraff, “Fleet-Management & Vehicle Routing in Real-time using Parallel Computing Algorithms”, International conference on soft computing and signal Processing" at Malla Reddy College of Engineering and Technology(June 22-23), 2018, Hyderabad, India
2. Adwaith Sreekumar, K S Arun Adithiyan, **Mohanasundaram R**, K.S.Sivaranjane, “Task Scheduling for Multi-Core processors using HCDDSL Algorithm”, International Conference on Recent Technologies in Electrical Engineering (ICRTEE – 2018) during 03/03/2018 and 04/03/2018 by ARM College of Engineering and Technology, Chengalpattu.
3. K S Arun Adithiyan, Adwaith Sreekumar, **Mohanasundaram R**, K.S.Sivaranjane “Serial and Distributed Execution in Big Data: A Comparative Study using Hadoop (Distributed) and Python (Serial)”, International Conference on Recent Technologies in Electrical Engineering (ICRTEE – 2018) during 03/03/2018 and 04/03/2018 by ARM College of Engineering and Technology, Chengalpattu.
4. N.Suresh Kumar, **Dr. Mohanasundaram.R**, M.P.Gopinath, “ Copy Move Forgery method based SHIFT to duplication identification in image region”, International Conference on

- Inventive Computing Systems and Applications (ICICSA 2018) during January 29-30, 2018 by Inventive Research Organization, Thailand (**Acceptance Received**)
5. **R.Mohanasundaram**, Dr.P.S.Periasamy “A Meta heuristic Algorithm for Optimal Data Storage Position in Wireless Sensor Networks”, 2016 International Conference on Innovations in information, Embedded and Communication Systems (ICIECS), 17&18 March 2016 at Karpagam Engineering College, Coimbatore.
 6. A.JayanthilaDevi, **Dr.Mohanasundaram**, Dr.Sakthivel, “Spectrum Holes Sensing: (Another type of "White Spaces" Challenges in Cognitive Radio network)” conference of communication, cloud and big data CCB 2016 , Sikkim Manipal Institute of Technology (SMIT) at Majitar, Sikkim during NOVEMBER 10-11, 2016.
 7. **R.Mohanasundaram**, Dr.P.S.Periasamy “Embedded Based Home Personal Computer Access and Control using Virtual Network Computing” , International Conference on Cloud Computing and Services 2011 (ICCCS 2011) held on Dec 14 & 15 , 2011 at Savitha Engineering College, Chennai.
 8. **R.Mohanasundaram**, Dr.P.S.Periasamy “A Meta heuristic Algorithm for Optimal Data Storage Position in Wireless Sensor Networks”, 2016 International Conference on Innovations in information, Embedded and Communication Systems (ICIECS), 17&18 March 2016 at Karpagam Engineering College, Coimbatore.

NATIONAL CONFERENCES:

1. **R.Mohanasundaram**, M.Sreedhar “Embedded Based Home PC Access and Control Using VNC through GSM” in the 5th National Conference on “Application of Emerging Technologies” during 24 - 25 March 2008 at Adhiyamaan College of Engineering, Hosur, Tamilnadu, India.
2. **R.Mohanasundaram**, M.Sreedhar “Embedded Based Home PC Access and Control Using VNC through GSM” in the 1st National Conference on “Computing and Communication Technologies” on 21st March 2009 at Surya Engineering College, Mettukkadai, Erode.
3. **R.Mohanasundaram** “A Compact AES Encryption Standard on Xilinx FPGA” in the 2nd National Conference on “Computing and Communication Technologies” on 20st March 2010 at Surya Engineering College, Mettukkadai, Erode.
4. **R.Mohanasundaram**, N.Vijay “Implementation of ASSIST in Embedded based Medical Technology” in the 1st National Conference on “Trends in Electronics, Instrumentation, Embedded System and Automation” on 27th March 2010 at Karunya University.
5. **R.Mohanasundaram**, P.R.K.Kavithasri “ Small Delay Defect Detection Using Signature Register”, National Conference on Innovaations in Electrical and Electronics Engineering (IEEE'12) on 21.01.2012 at M.P.N.M.J Engineering College, Chennimalai.
6. B.Sudharsan, **R.Mohanasundaram**, “Monitoring and Control of Green House Parameters by using Wired/Wireless Networking Infrastructure”, National Conference on Innovaations in Electrical and Electronics Engineering (IEEE'12) on 21.01.2012 at M.P.N.M.J Engineering College, Chennimalai.
7. **R.Mohanasundaram**, P.R.K.Kavithasri “Small Delay Defect Detection Using Signature Register”, National Conference on Communication Technology Interventions for Rural and

Social Development(NCCTIRD-2012) during 16.02.2012 & 17.02.2012 at Sri Krishna College of Engineering and Technology, Coimbatore.

8. R.Arun, **R.Mohanasundaram**, “EGMP: Multicasting Routing Protocol With QOS Improvements”, National Conference on Communication Technology Interventions for Rural and Social Development (NCCTIRD-2012) during 16.02.2012 & 17.02.2012 at Sri Krishna College of Engineering and Technology, Coimbatore.

BOOKS / CHAPTERS:

1. A.Mummorthi, R.Mohanasundaram, Shubam Saraff, “Fleet-Management & Vehicle Routing in Real-time using Parallel Computing Algorithms”, Soft Computing and Signal Processing, AISC, volume 898, pp 263-270, February 2019.
2. **Dr. R. Mohanasundaram**, Dr.A.Jayanthiladevi, Keerthana.G, Chapter: Software Defined Cloud infrastructure, Book Title: Research on Cloud and Fog Computing Infrastructures for Data Science, pp:108-123, 2018, IGI Global Publishers, USA.
3. **Dr. R. Mohanasundaram**, Dr. P. S. Periasamy, R. Arun, “Enhanced Bat Algorithm In Wireless Sensor Networks”, Advanced Engineering Research and Applications (AERA), Chapter Code: 1151, Research India Publications, Delhi.

CONFERENCES ORGANIZED:

2. <http://www.icafe.com/big-data-cloud-computing.php> - Session Chair and Keynote speaker for an International Symposium on "Big Data, Cloud Computing and Multi-Core Embedded System" in 1st International Conference on Water Resources and Sustainability (ICWRS) & 3rd International Conference on Alternative Fuels, Energy and Environment (ICAFEE): Future and Challenges during OCTOBER 28-31, 2018, NANJING, CHINA.
3. <https://www.indiaconf.org/> - Session Chair, Special session on “Big data and Cloud Computing” in 5th International Conference On Information System Design And Intelligent Applications, India (2018), 19th - 21st July, 2018, Organised By Université Des Mascareignes, MAURITIUS.
4. <http://www.sci2018.in/> - Special Session Chair - Big Data, Cloud Computing and Multi-Core Embedded System (BccMES) in **SCI2018** 3rd International Conference On Smart Computing & Informatics (SCI), 21,22 December 2018.
5. <http://www.icscsp.com/pdf/specialsession.pdf> - Special session Organizer - special session on "Big Data and Cloud Computing" - International conference on soft computing and signal Processing" at Malla Reddy College of Engineering and Technology(June 22-23), 2018, HYDERABAD, INDIA

PROJECTS AND SEMINAR GRANTS:

S.No	Name of the sponsor	Title of Project/Seminar	Fund Received in Rs.
1	DRDO	Seminar on Embedded Wireless Sensor Networks	25,000.00 [ERIP/ER/11-12/C/406/M/01]

2	Anna University	FDP on Communication theory	Rs.70,000 15/CFD/Winter/FDTP/2012-44
3	AICTE	Applications Of Wireless Sensor Network In Industry And Consumer Electronics	Rs.200,000
4	DRDO	Two days' workshop on Research challenges in WISENET	Rs.30,000 ERIP/ER/12-13/C/473
5	CSIR	International Conference	Rs.40,000 SYM/8007/13-HRD

TEACHING INTEREST AND SUBJECT TAUGHT

For M.Tech/M.E Programme:

- MCDA (Microcontroller system Design and Applications)
- ESD (Embedded System Design)
- RTS (Real Time System)
- ECS (Embedded Control Systems)
- Embedded System Lab-I
- Embedded System Lab-II
- DECS (Design of Embedded Control Systems)

For B.Tech/B.E Programme:

- Python Programming
- Microprocessor and Interfacing
- Networking protocols
- Digital logic and principles
- Wireless sensor networks
- Wireless communication
- Integrated Circuits lab.
- Computer architecture and organization
- Engineering Practice Lab (Electrical)
- EE (Electrical Engineering)
- DS&OOPS (Data Structures and Object Oriented Programming)
- DS&OOPS Lab
- ECED (Electric Circuits and Electron Devices)
- Microprocessor and Microcontrollers lab
- ECED (Electric Circuits and Electron Devices) lab
- MRISC (Microcontroller and RISC Architecture)
- Data Structures and Algorithm

For Bsc Programme:

- Computer Architectures.
- Digital Logics and Computer Design (theory and lab)
- Data Structures and Algorithm
- Fundamentals of Computer design
- Microprocessors

RESPONSIBILITIES:

- **Internal Committee member during UGC & AICTE** Visit in the AY 2016-17
- **Faculty coordinator in GRAVITAS'2017**(An international Knowledge Carnival) @ VIT.
- **Year Co-ordinator** for B.Tech(CSE) first years.
- **Attendance Warden** for VIT Men's hostels from May 2017 to till date.
- **B.Tech Project Co-ordinator** for the dept of software systems from July 2017 to till date
- **Final Year (Capstone/SAP/Internship) Project coordinator** for B.Tech(CSE)
- **IQA (Internal Quality Audit) committee member** in the AY 2016- 17

- NAAC – MOU file in-charge [Component 43, 47, 48, 49]
- **Internal Board of Study member** at VIT University and K.S.R. College of Engineering.
- **Doctoral Committee member** for three research scholars in Anna University.
- **Time Table Committee member of SCSE at VIT University** from April 2013 to till now.
- NAAC Course files in-charge at VIT University.
- **VIT Men’s Hostel Deputy warden from 2013.**
- **AICTE Proposal incharge at K.S.R.College of Engineering** from June 2011 to Dec 2012.
- **Department Placement co-coordinator** at K.S.R.College of Engineering from June 2011 to Dec 2012.
- **Time Table Coordinator** at K.S.R.College of Engineering from Jan 2012 to Dec 2012.
- **Staff Coordinator** for EEE Student’s Association during the academic year 2008-2009.
- **Industrial Visit Coordinator** for EEE Dept during the academic year 2008-2009.
- **Test Coordinator** in EEE Department 2008-2010.
- **Magazine Coordinator** of EEE Department during the Academic year 2008-2009.
- **Staff coordinator** for VCET Students Alumni Association.

PARTICIPATION IN SEMINARS / WORKSHOPS/VALUE ADDED COURSES:

S.No	Nature of program	Topic / Title	Organized by	Period	No.of Days
1.	Refresher Course	Problem solving and programming using Python and its industrial Applications	SCOPE, VIT, Vellore	04.07.2018 & 05.07.2018	2
2.	FDP	Digital PAD	ASC, VIT-Vellore	10.01.2018	1
3.	Workshop	Emerging Trends in Theoretical Computer Science	School of Computer Science and Engineering, VIT, Vellore	07.12.2017 to 08.12.2017	2
4.	Certificate course	Effective Patent drafting	CIPR, Anna University, Chennai	22.11.2017 to 26.11.2017	5
5.	Training Program	Interfacing Peripherals with Zybo –FPGA Board	SENSE, VIT, Vellore	03.11.2017 to 04.11.2017	2
6.	Workshop	PLC Automation and SCADA	Navodaya Institute of Technology, Raichur, Karnataka	31.08.2017 to 01/09/2017	2
7.	Workshop	SMART board – Note Book Software	ASC, VIT-Vellore	10.01.2017	1
8.	Workshop	Project based Learning (PBL)	ASC, VIT-Vellore	13.12.2016	1
9.	Workshop	High order thinking skills (HOTS)	ASC, VIT-Vellore	12.12.2016	1
10.	FDP	FDP on LaTeX	SCOPE, VIT University	16.11.2016	1

11.	FDP	FDP on Mathematical Tools for Signal, Image Processing and Communications	SENSE, VIT University	15.07.2016 to 17.07.2016	3
12.	Workshop	Competency Mapping for Effective Teaching	ASC, VIT-Vellore	07.04.2016	1
13.	FDP	How to set Question Papers for an Open book test	ASC, VIT-Vellore	25.09.2015	1
14.	FDP	Tender Loving Care	ASC, VIT-Vellore	14.07.2015	1
15.	ISTE Workshop	Computer Networking	IIT Bombay and VIT University	28.05.2014 to 05.06.2014	8
16.	FDP	Effective Paper for Journal	VIT University	17.06.2014	1
17.	FDP	MATLAB	VIT University	27.08.2013 to 29.08.2013	3
18.	FDP	Recent Trends in Data Mining Tools, Applications and Research Issues	VIT University	28.06.2013 & 29.06.2013	2
19.	Seminar	Low power VLSI Design	JCT College of Tech, CBE	01.10.2012	1
20.	Seminar	Recent trends and research challenges in Wireless Networks	PSG Tech, CBE	28.09.2012 to 30.09.2012	3
21.	Workshop	Educator's day 2011	National Instruments & NICE at Bangalore	12.10.2011 & 13.10.2011	2
22.	Workshop	Industrial Automation using PLC and DCS	Kongu Engineering College	07.12.2009 to 09.12.2009	3
23.	Workshop	Computational Tools for Engineering Applications	Kongu Engineering College	21.05.2009 to 25.05.2009	5
24.	Workshop	Embedded Sensor Networks	CIT, Coimbatore.	01.02.2008 & 02.02.2008	2
25.	Implant Training	--	VI Microsystems Pvt Ltd, Chennai	02.06.2007 to 06.06.2007	5
26.	Course	VLSI modular course	SANDS Instrumentation Pvt Ltd, Chennai	09.01.2006 to 21.06.2006	6 Months
27.	Mini Project Exhibition	Gate Locker Security Systems	Dept of ECE, Velalar College of Engineering and Technology.	23.09.2005	1
28.	Seminar	Embedded Systems	Organ Technologies Pvt Ltd	01.02.2005	1
29.	Implant Training	--	SPB Paper Mill, Erode	14.05.2004 to 03.06.2004	20
30.	Mini Project Exhibition	--	Dept of ECE, Velalar College of Engineering and Technology.	16.02.2004	1

ADMINISTRATIVE COMPONENTS:

S.No	Name of the programme	Sponsored by	Duration
1	Computer Hardware Maintenance and Networking Concepts	Velalar College of Engg and Tech (VCET)	fifteen days
2	Hands on training program on Embedded Software Tools	VCET	20 th May 2009
3	Foundational Course on Embedded Systems	VCET	one day
4	PC Hardware Maintenance And Networking	VCET	fifteen days
5	Guest Lecture on Data Structures and Object oriented programming	K.S.R. College of Engineering	2 days
6	National Seminar on Embedded Wireless Sensor Networks	DRDO	2 days
7	Two days workshop on Research challenges in WISENET	DRDO	2 days
8	International Conference	CSIR	3 days
9	CAL Seminar Series on FPGA and VLSI	Self-Supportive	3 days
10	Value added program on Android Application Development	Self-supportive	2 days
11	Recent trends in computer architecture	VIT	One day
12	Recent trends in Digital logic	VIT	1 day
13	Applications of Multi core architecture	VIT	1 day
14	Parallel Processor Selection over wireless applications	VIT	1 day

PERSONAL DETAILS:

Date of Birth : 15-05-1984.
Father's name : A.Ranganathan
Mother's Name : R.Selvi
Address : 531, Uttukuli Road, Melappalayam, Chennimalai, Erode-638051, TN, India.
Age : 32
Marital Status : Married
Languages Known : English, Tamil, Chinese (Elementary level), Hindi ((Elementary level))
Nationality : Indian

DEPENDENT DETAILS:

Spouse Name and Qualification : K.S.Sivaranjane B.E., M.E., MBA.,
First Daughters' Name : M.Sanaa (DOB : 23/06/2011 & 5 Years old)
Second Daughters' Name : M.Nayanaa (DOB: 28/03/2015 & 2 Years old)

PASSPOST DETAILS:

Passport Number : K0414291

Date of Expiry : 01/04/2023

REFERENCES:

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DECLARATION:

I hereby declare that the above details are true to my knowledge.

Date :

Place: VIT, TN, India

(R.Mohanasundaram)